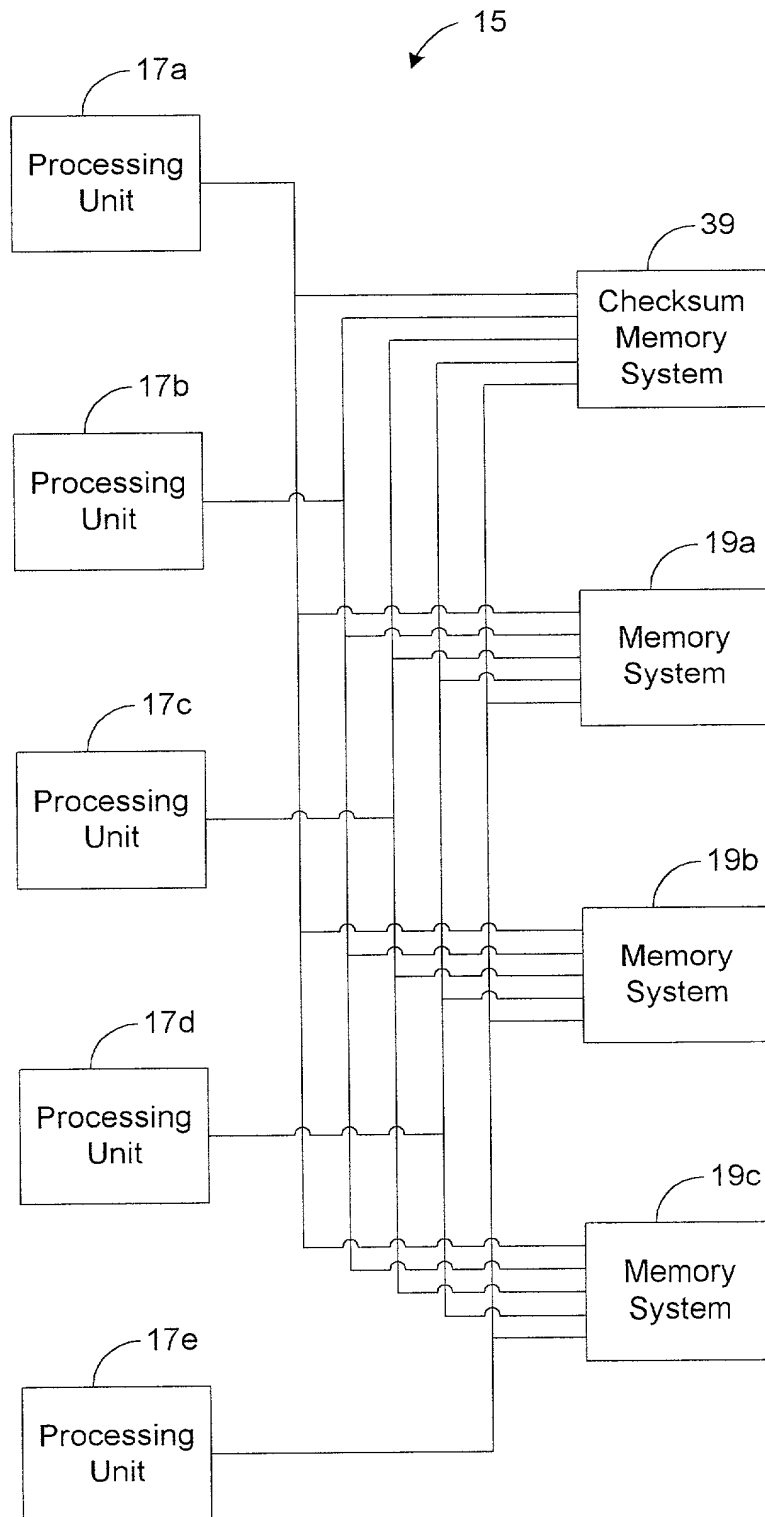


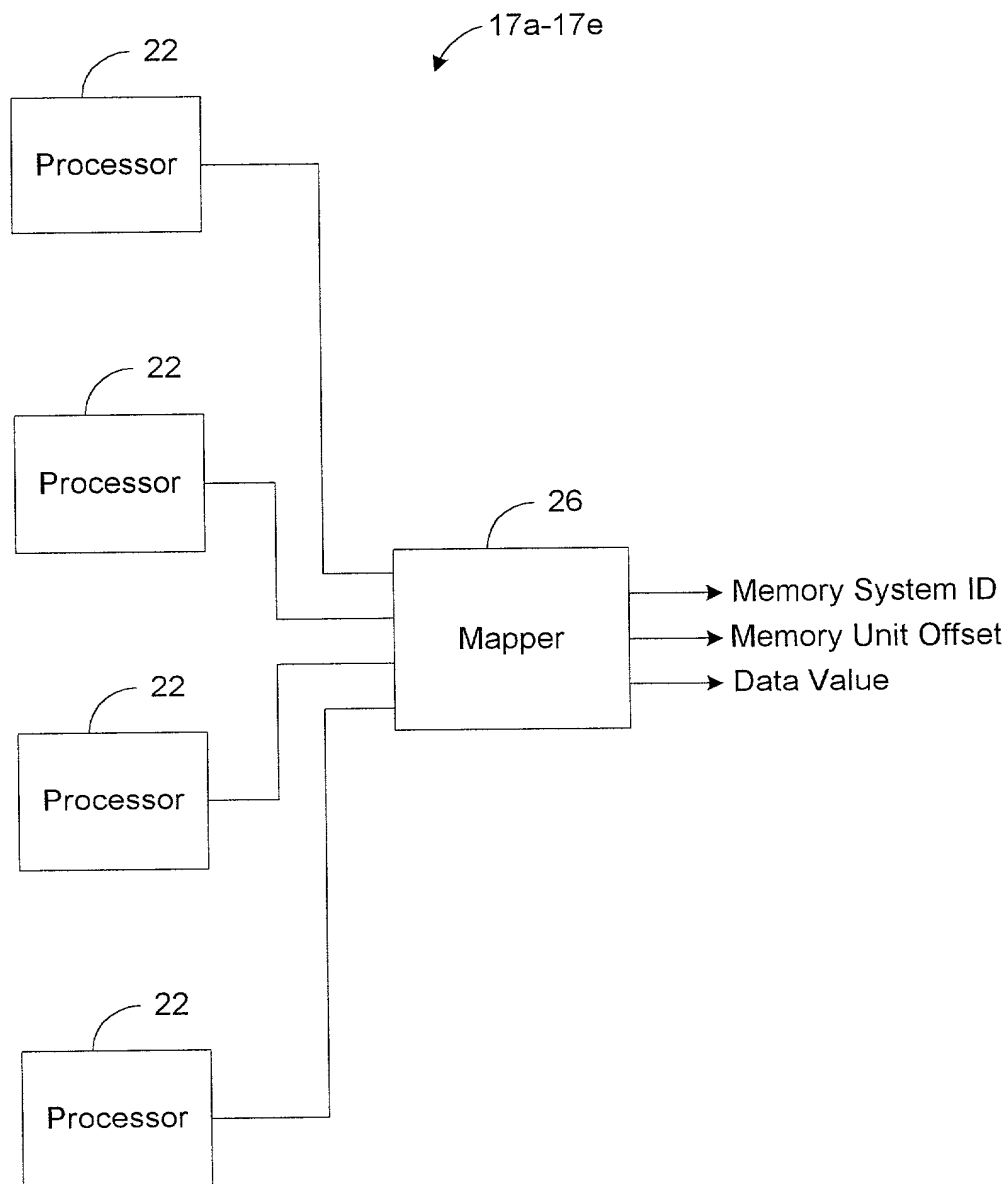
System And Method For Performing
Backward Error Recovery In A Computer

Inventor: Bryan Hornung et al

**FIG. 1**
(Prior Art)

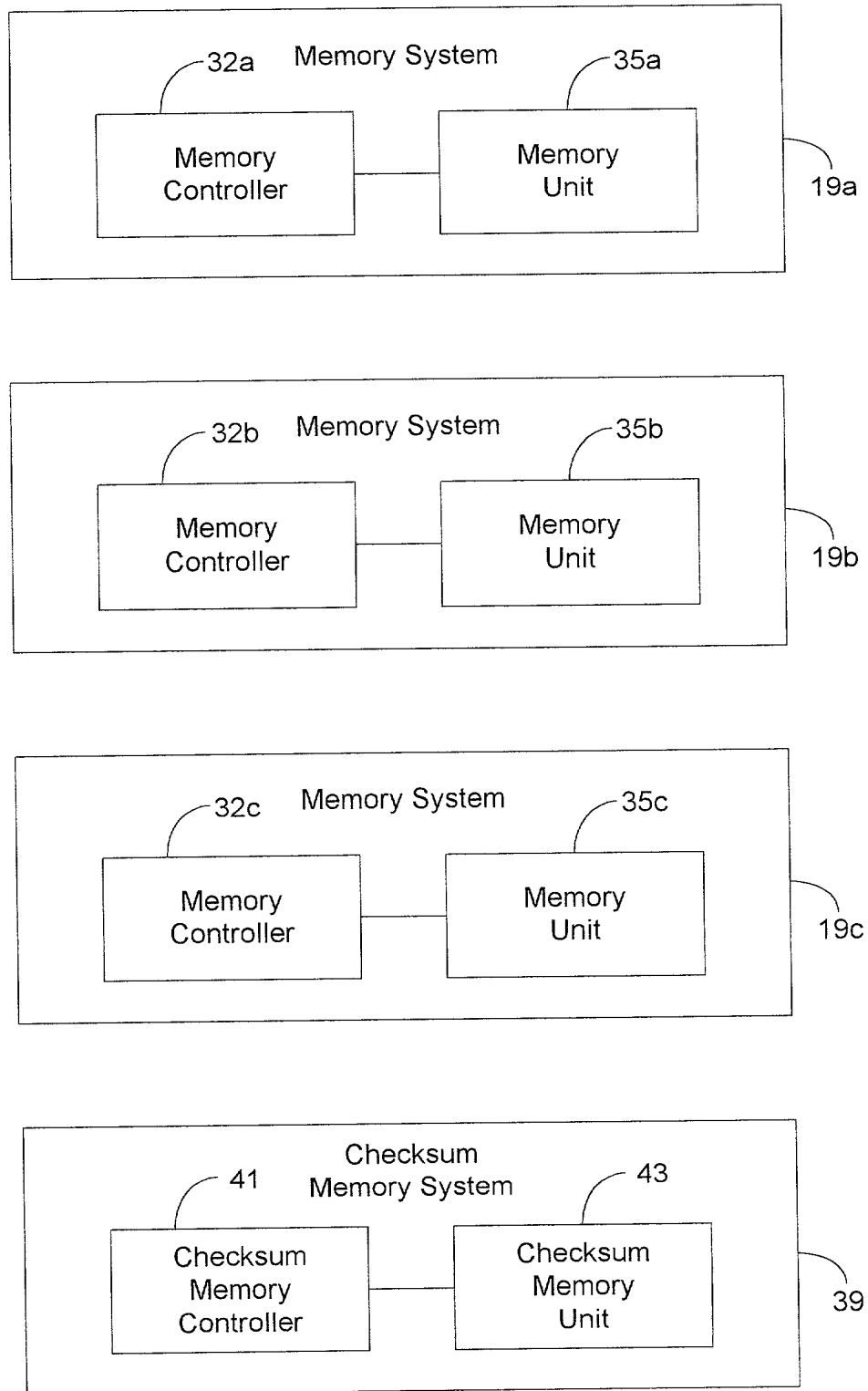
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**FIG. 2**
(Prior Art)

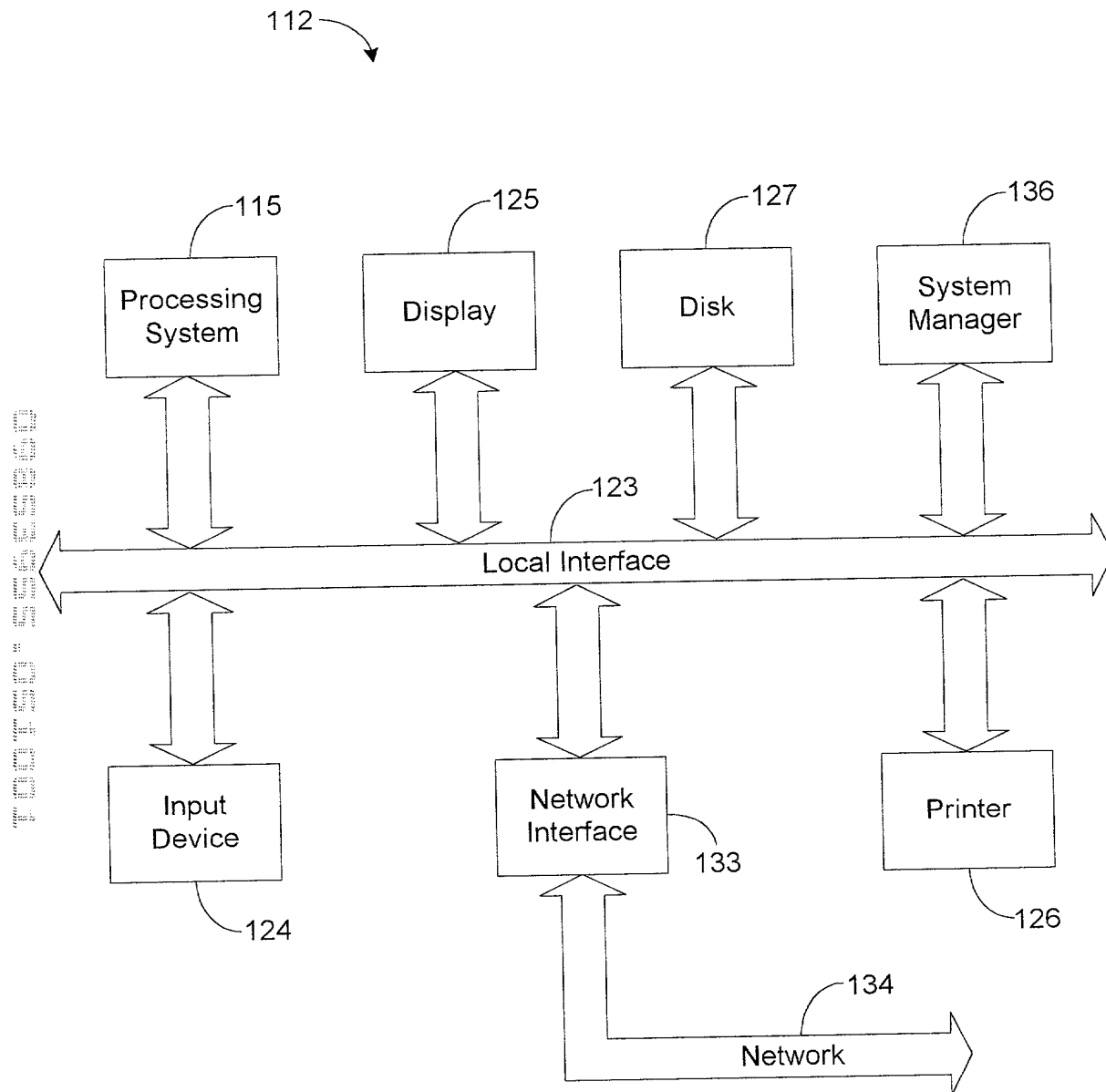
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**FIG. 3**
(Prior Art)

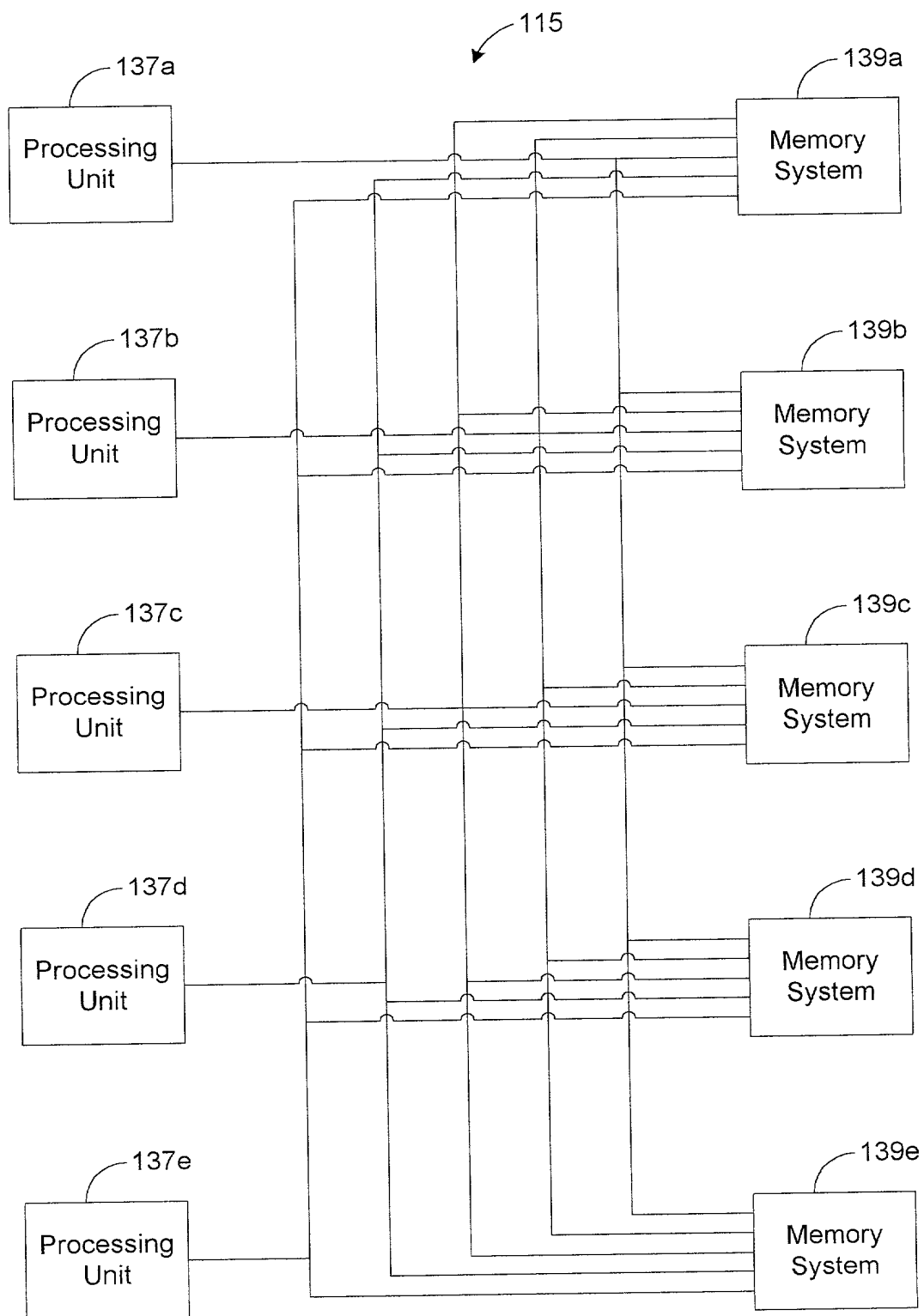
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**FIG. 4**

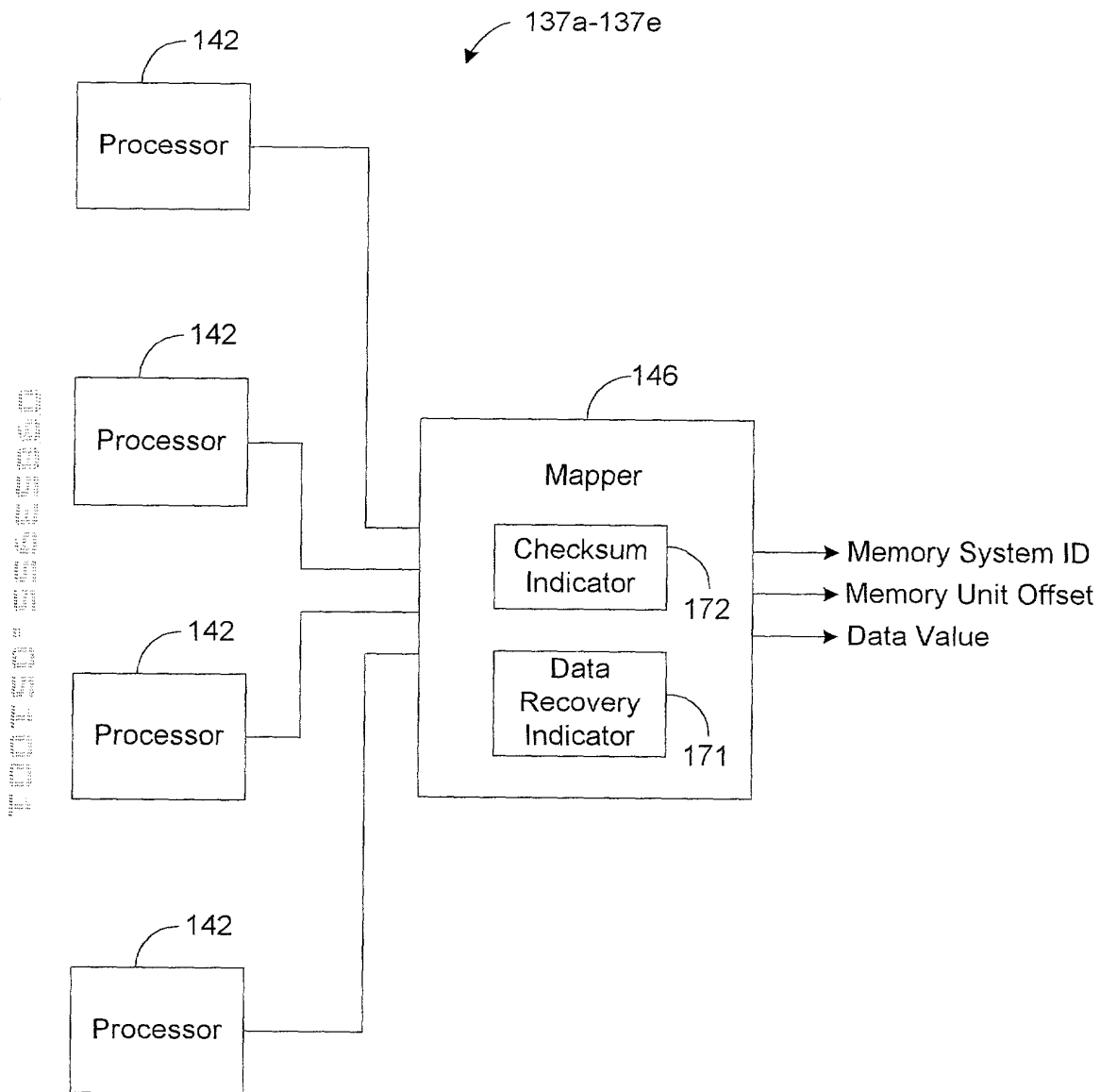
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**FIG. 5**

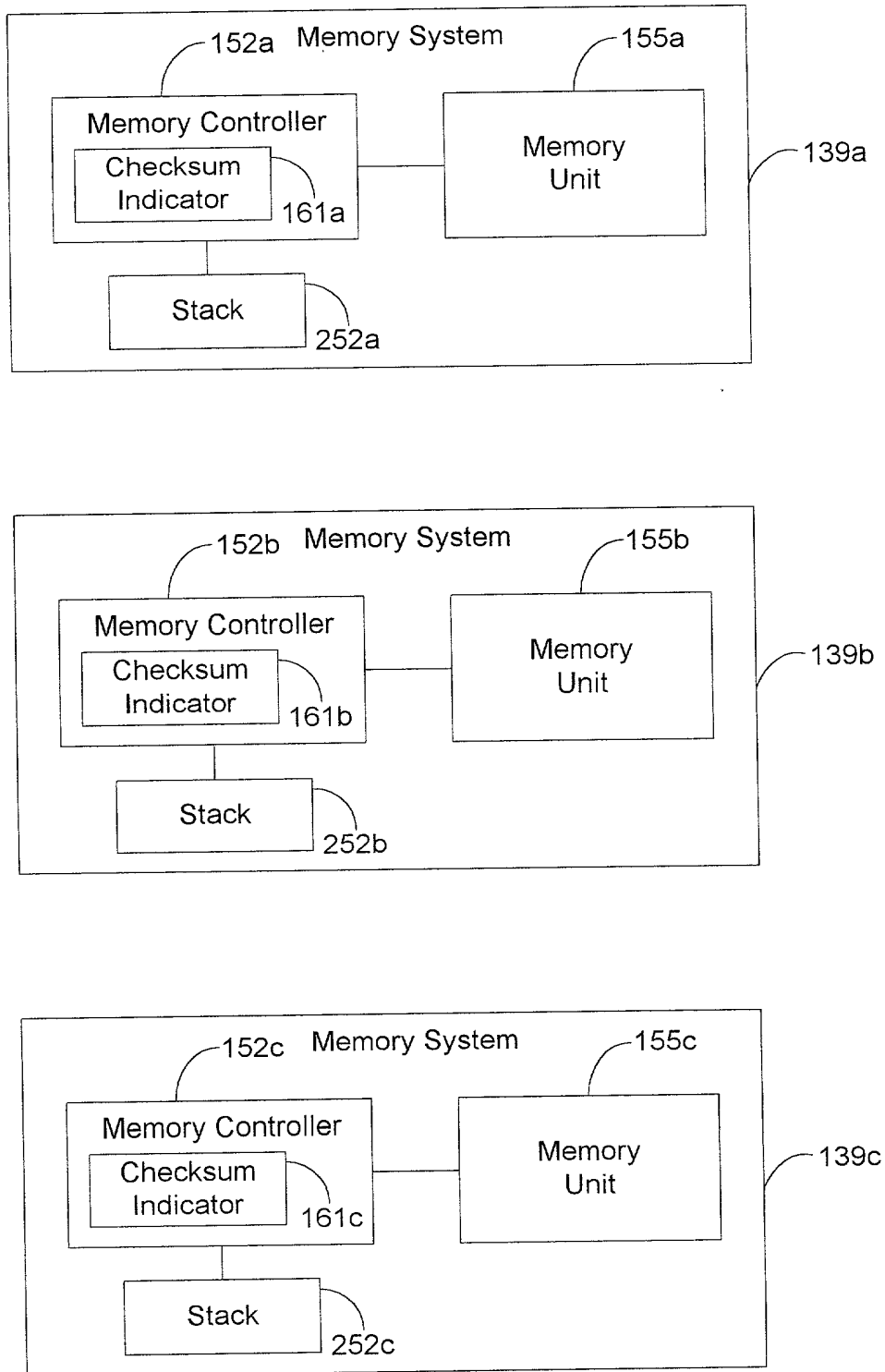
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**FIG. 6**

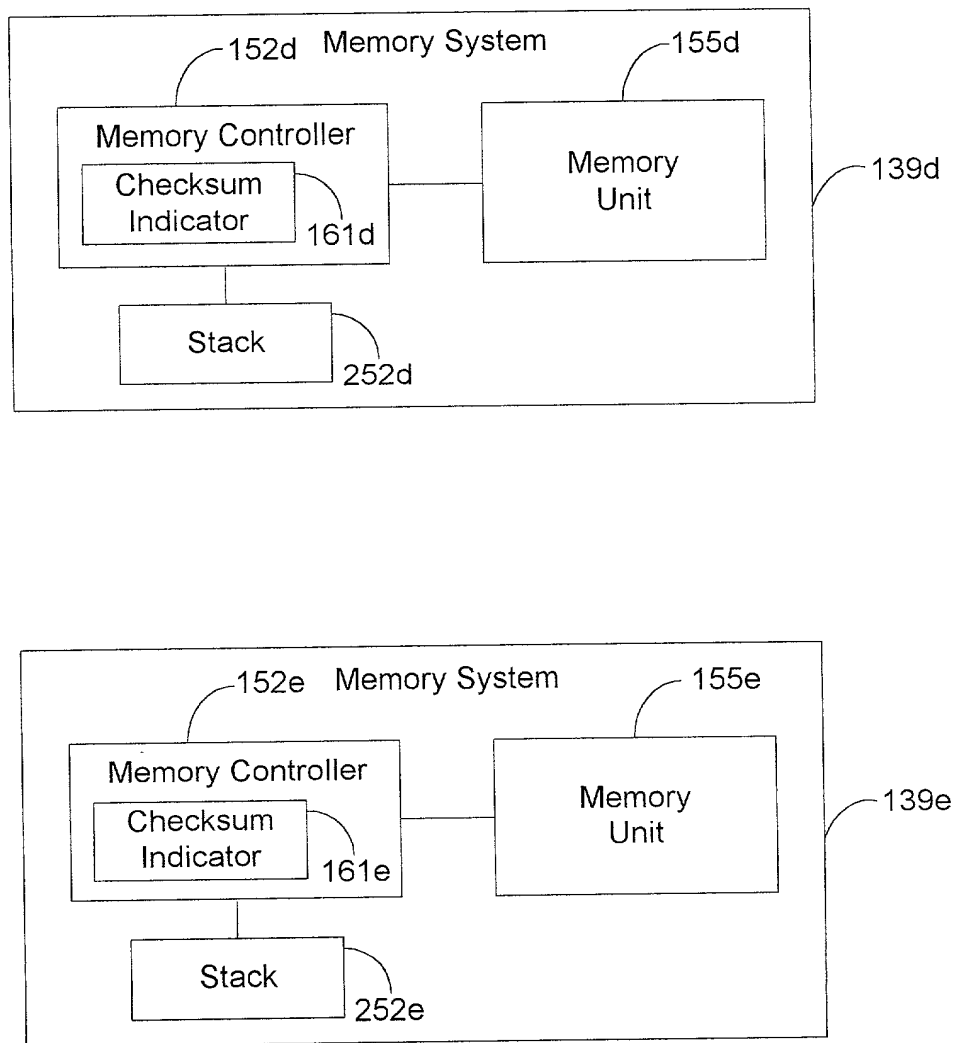
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**FIG. 7A**

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Backward Error Recovery In A Computer

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**FIG. 7B**

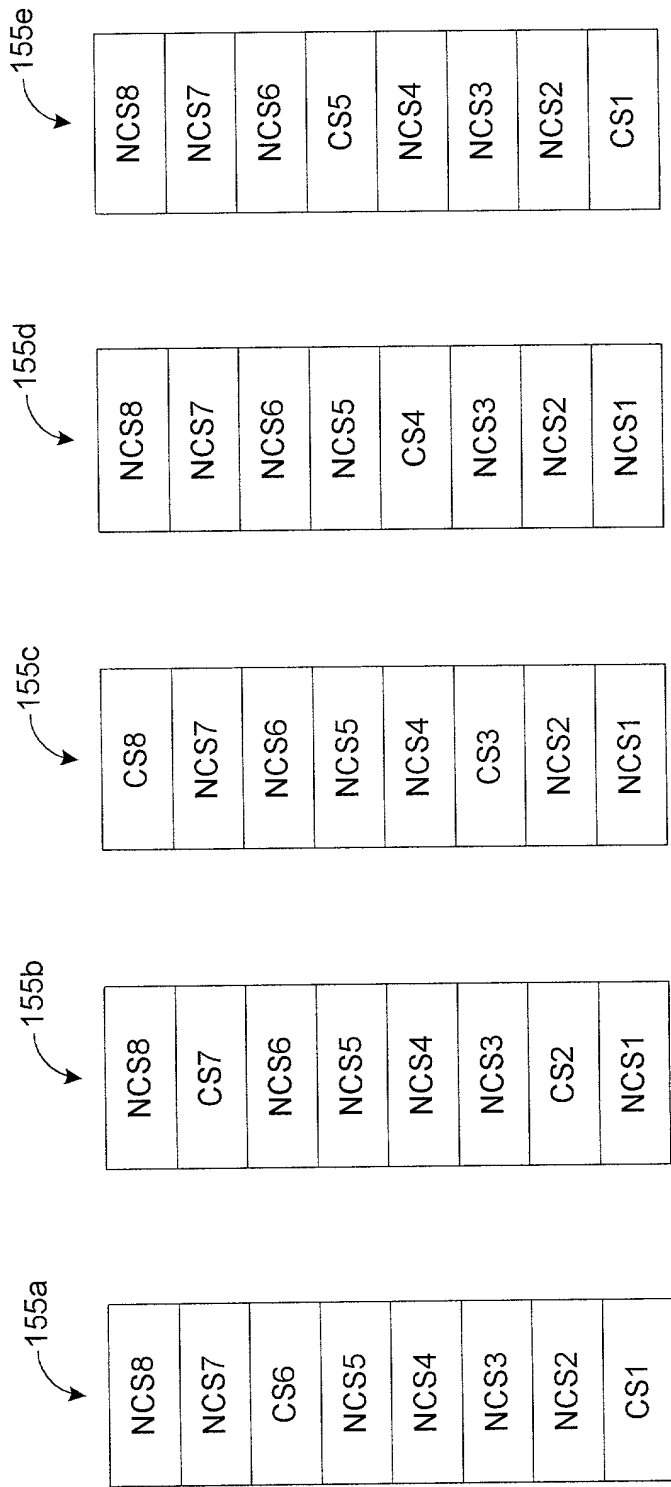
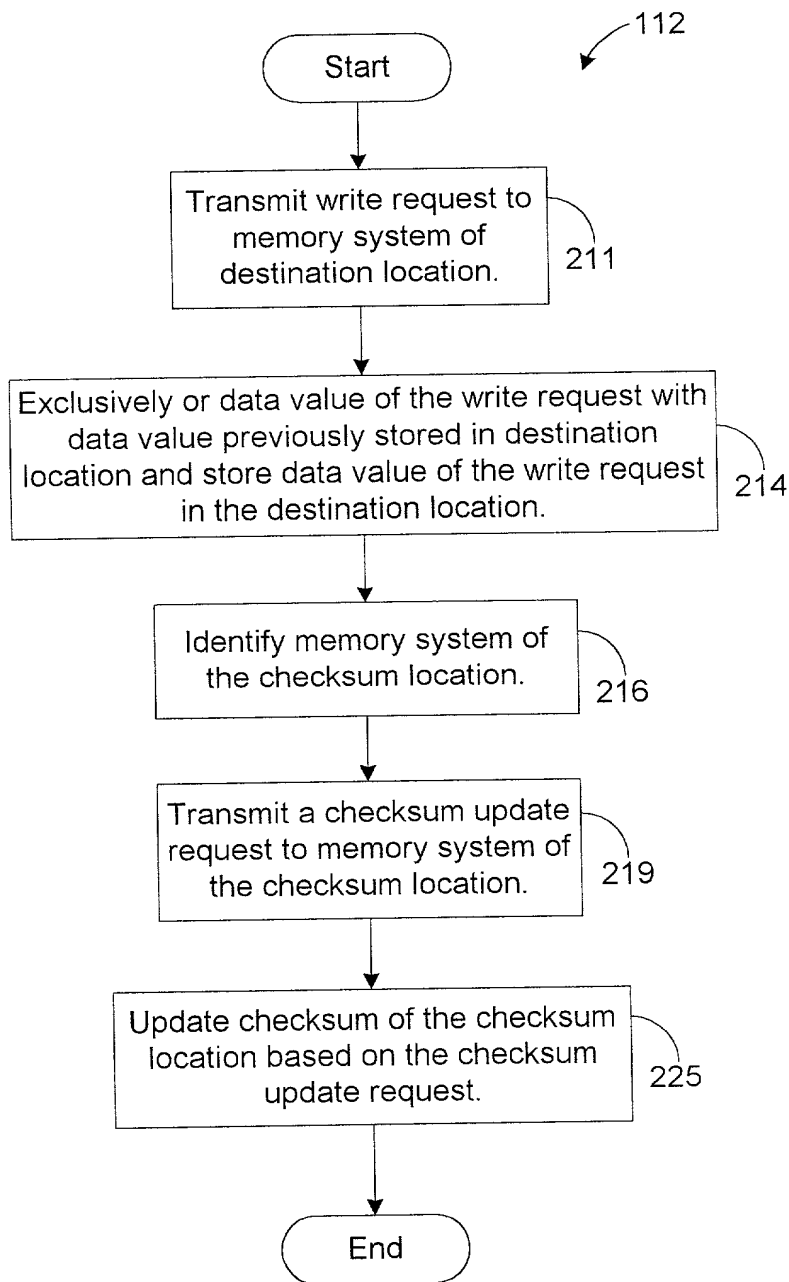


FIG. 8

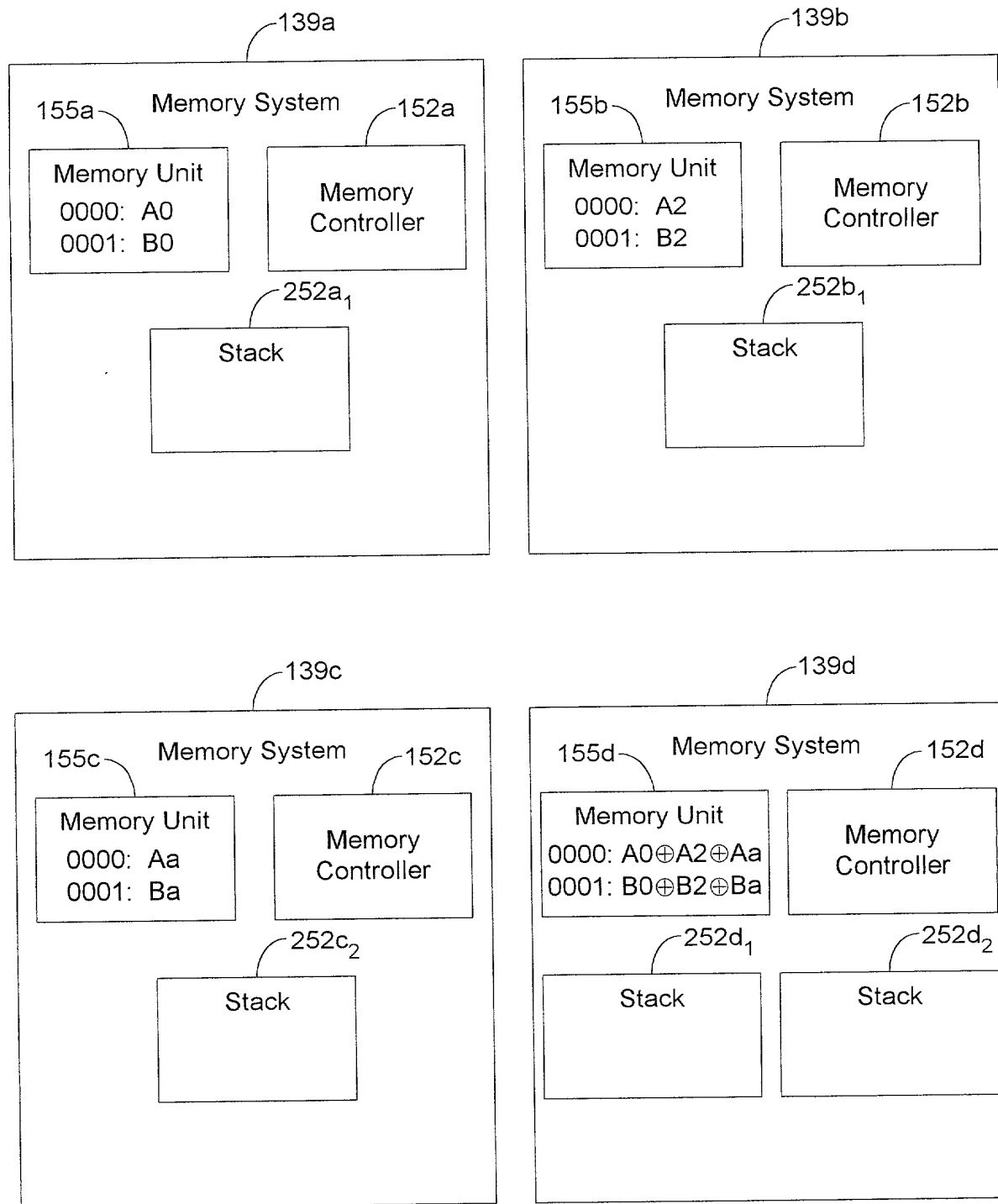
System And Method For Performing
Backward Error Recovery In A Computer

Inventor: Bryan Hornung et al

**FIG. 9**

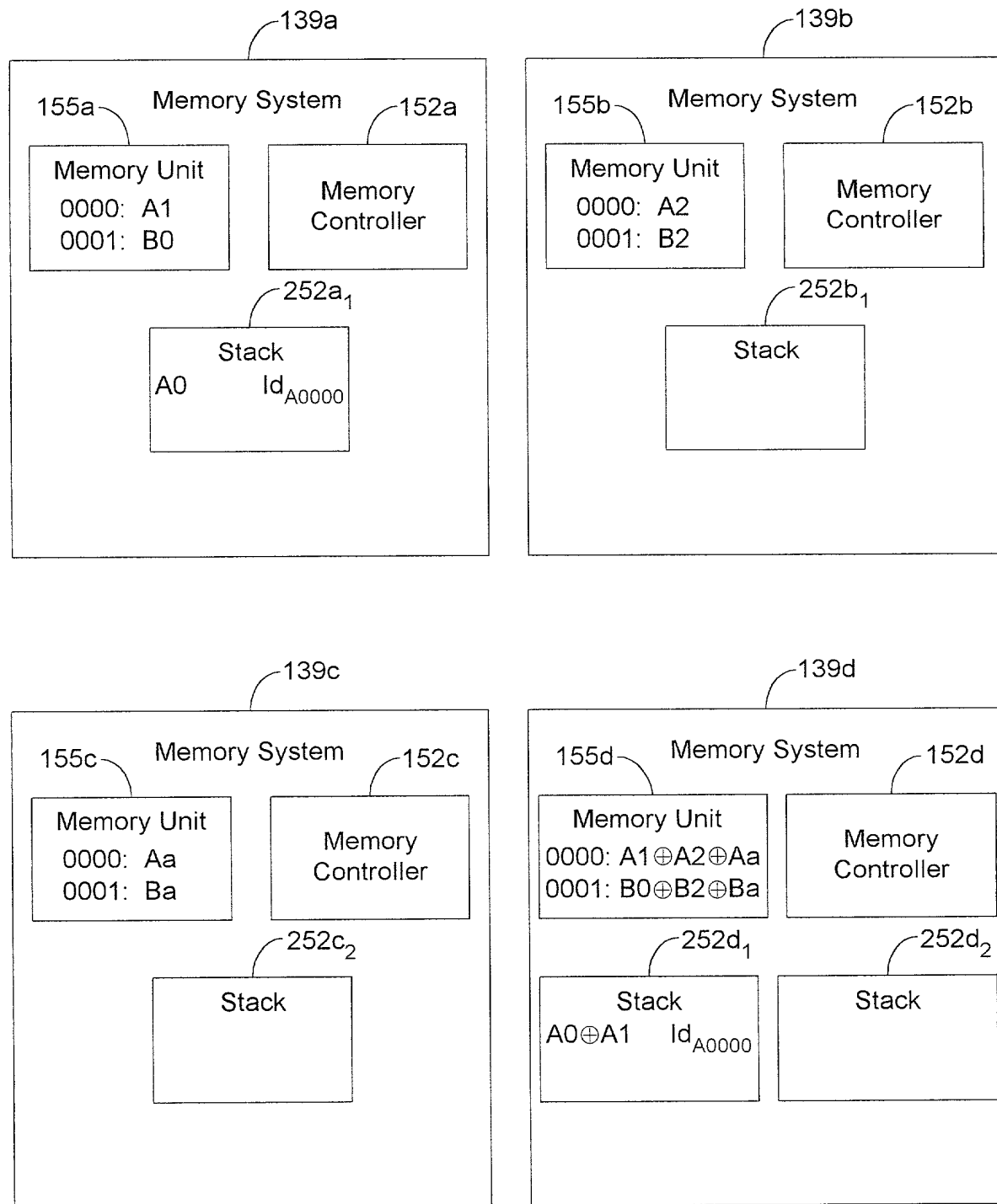
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**FIG. 10**

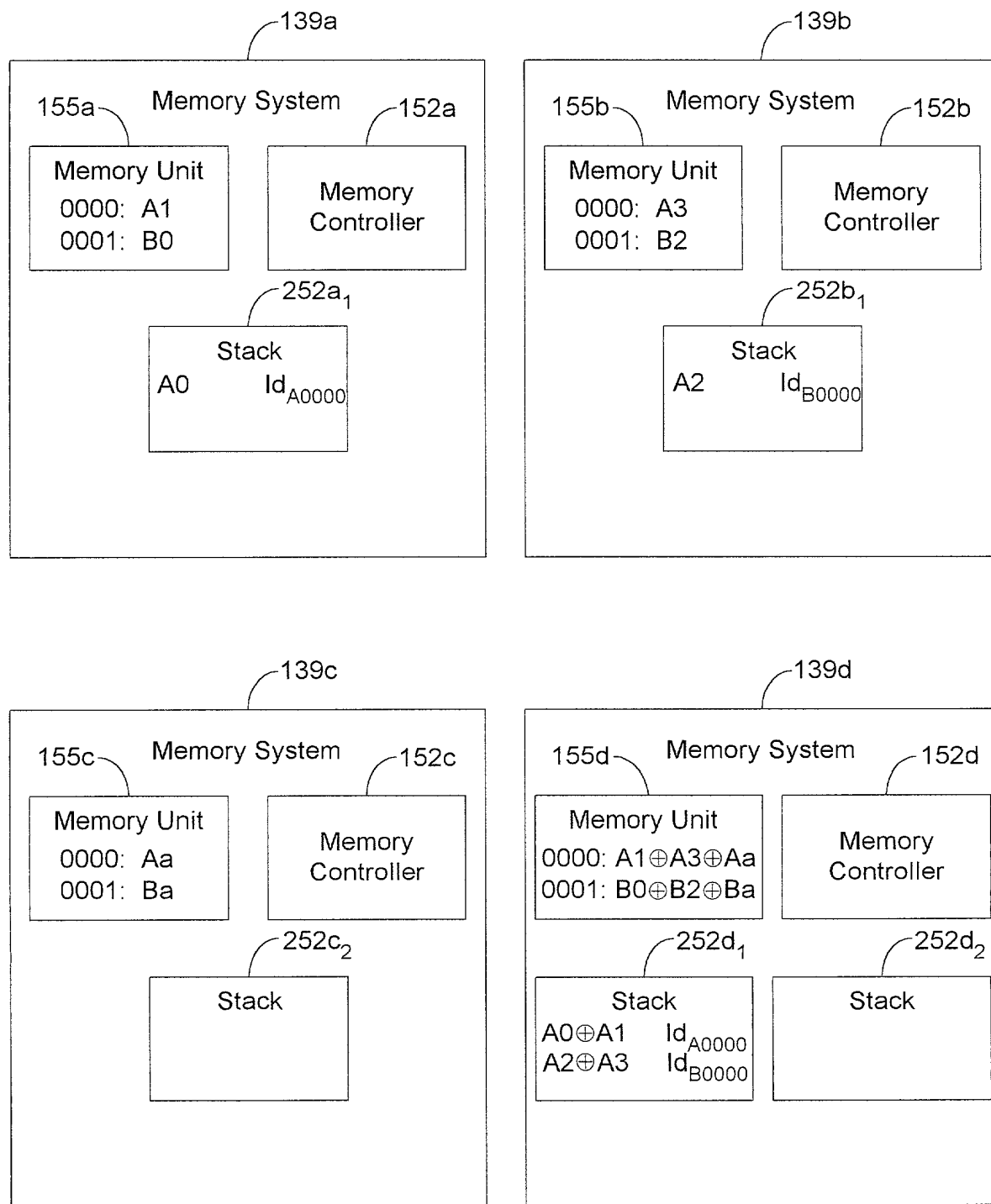
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**FIG. 11**

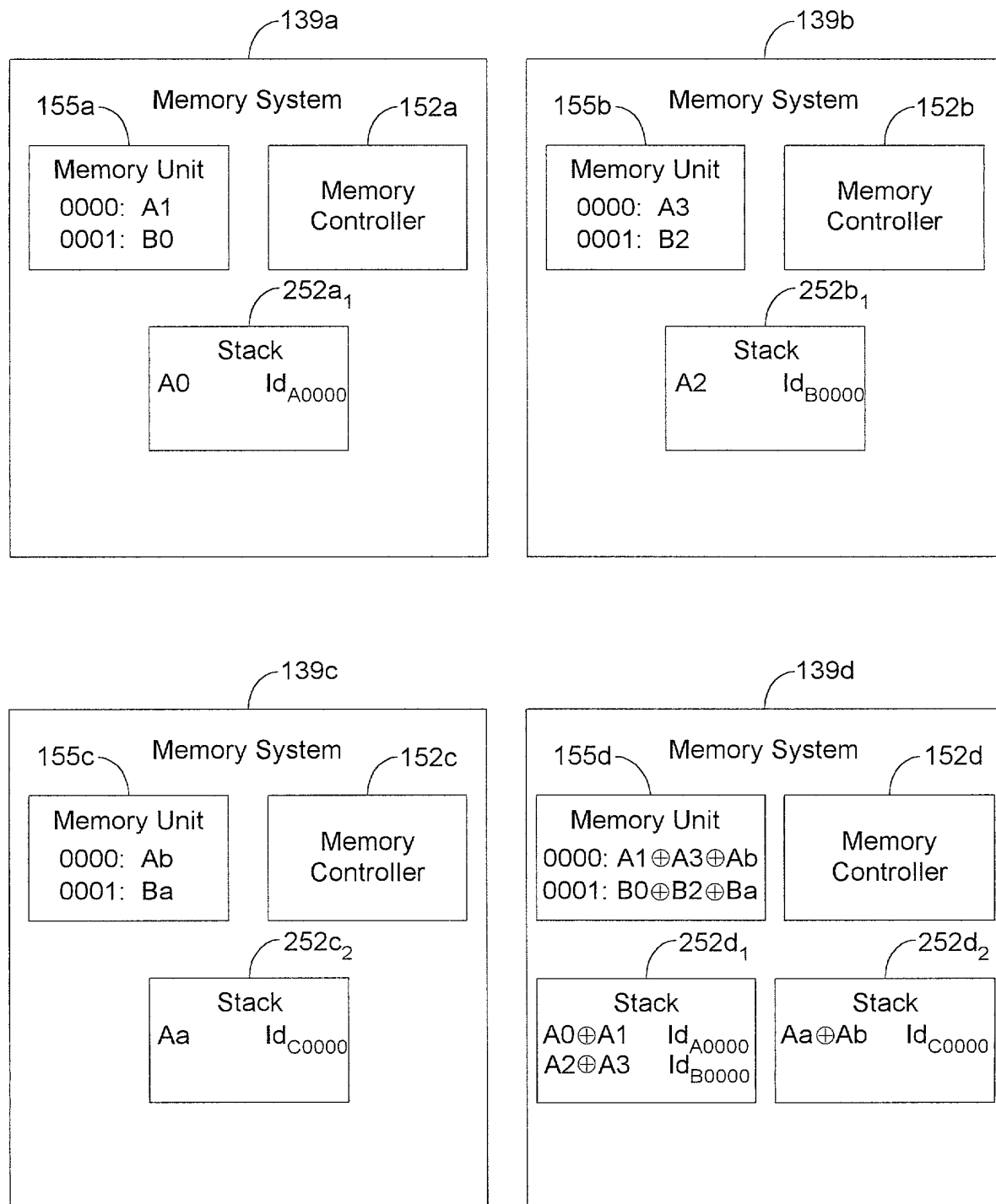
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**FIG. 12**

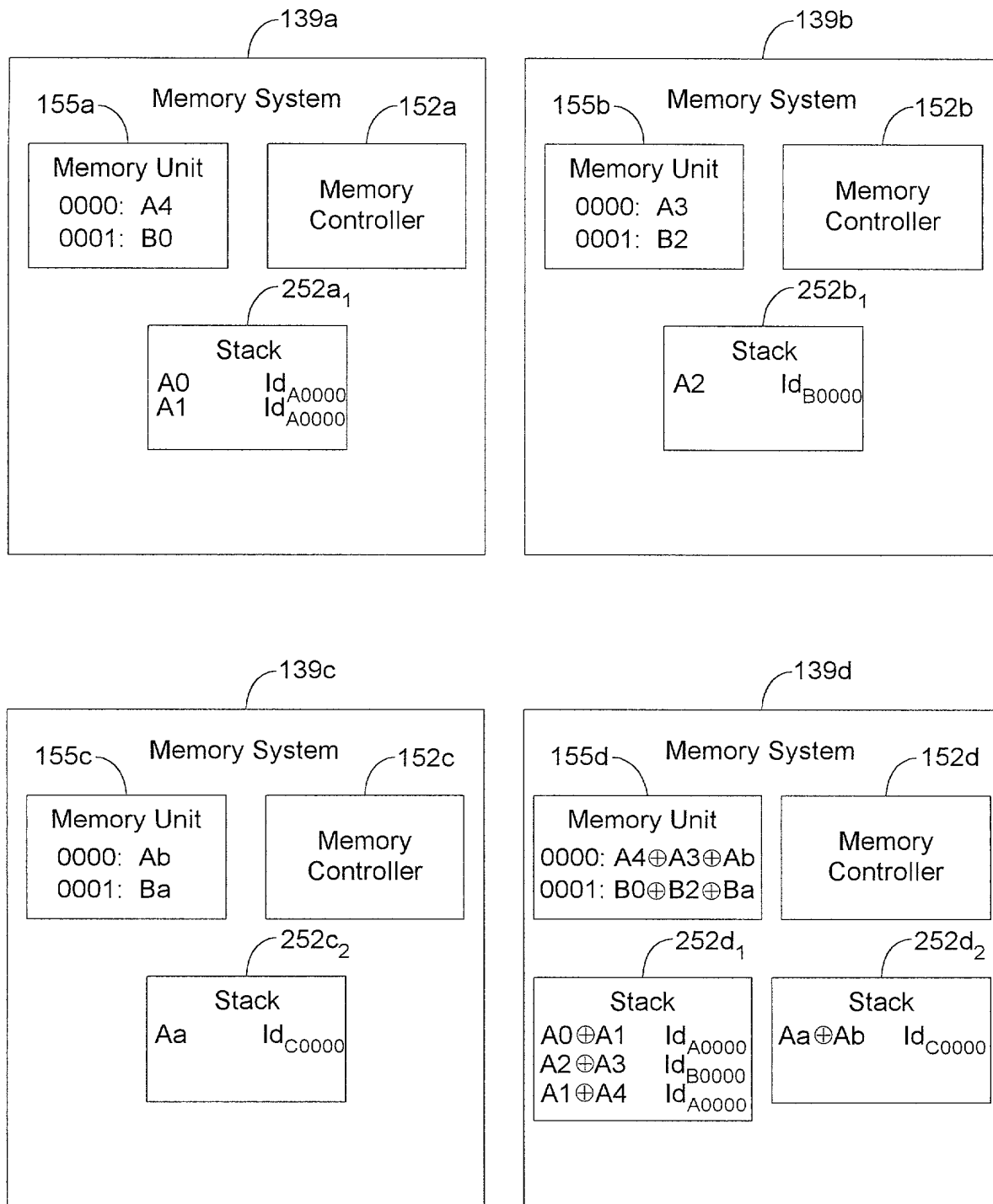
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**FIG. 13**

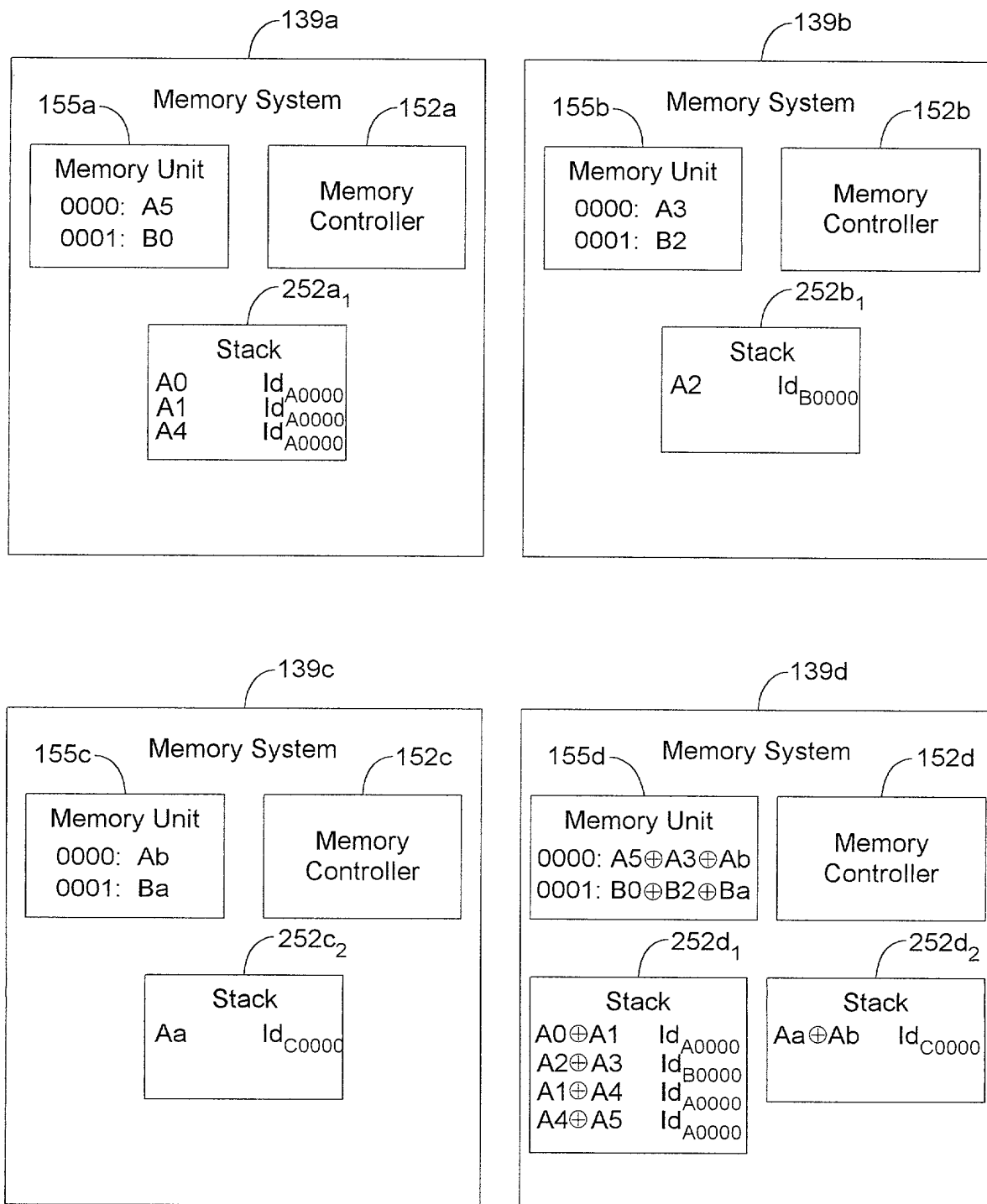
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**FIG. 14**

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**FIG. 15**

System And Method For Performing
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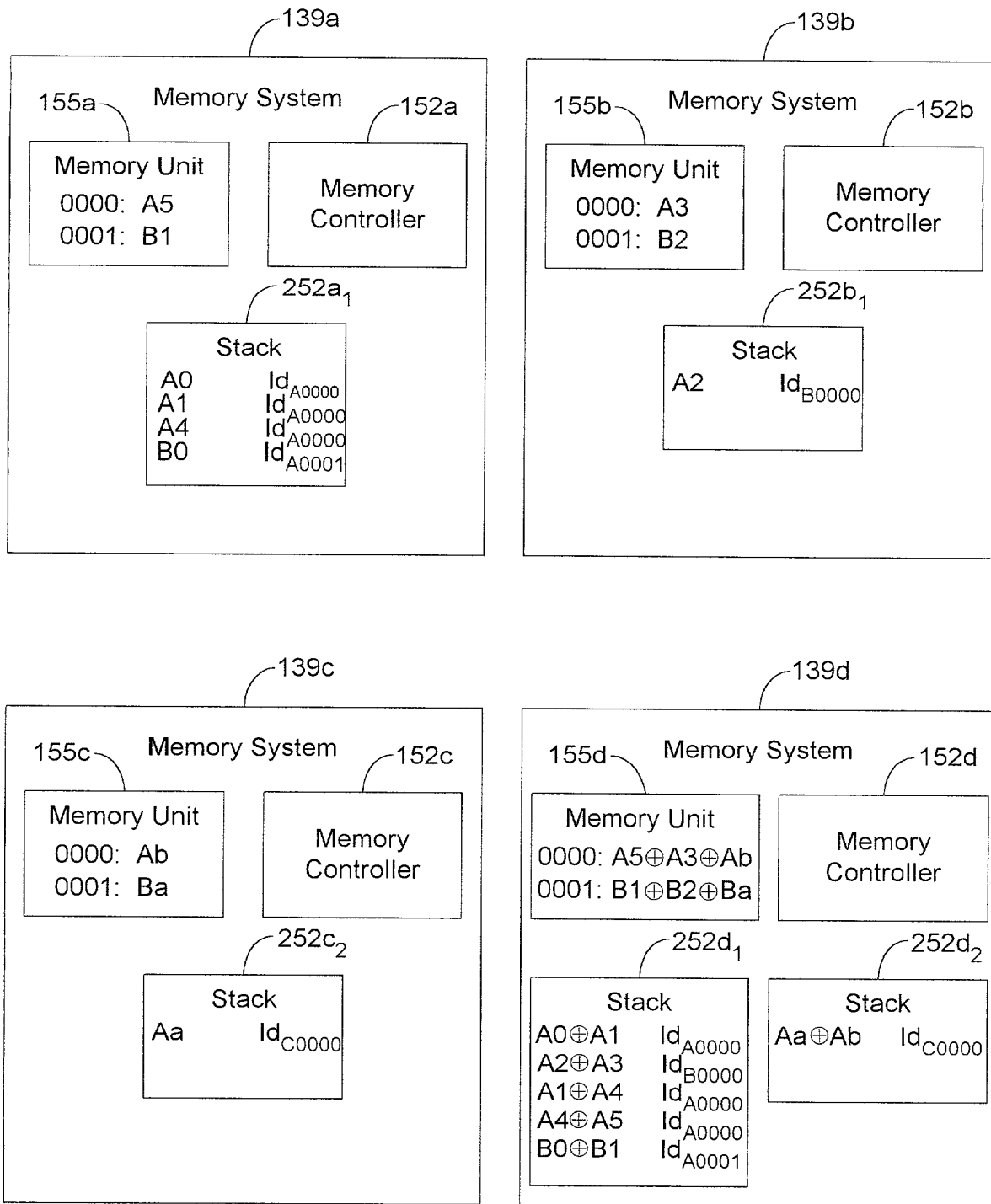
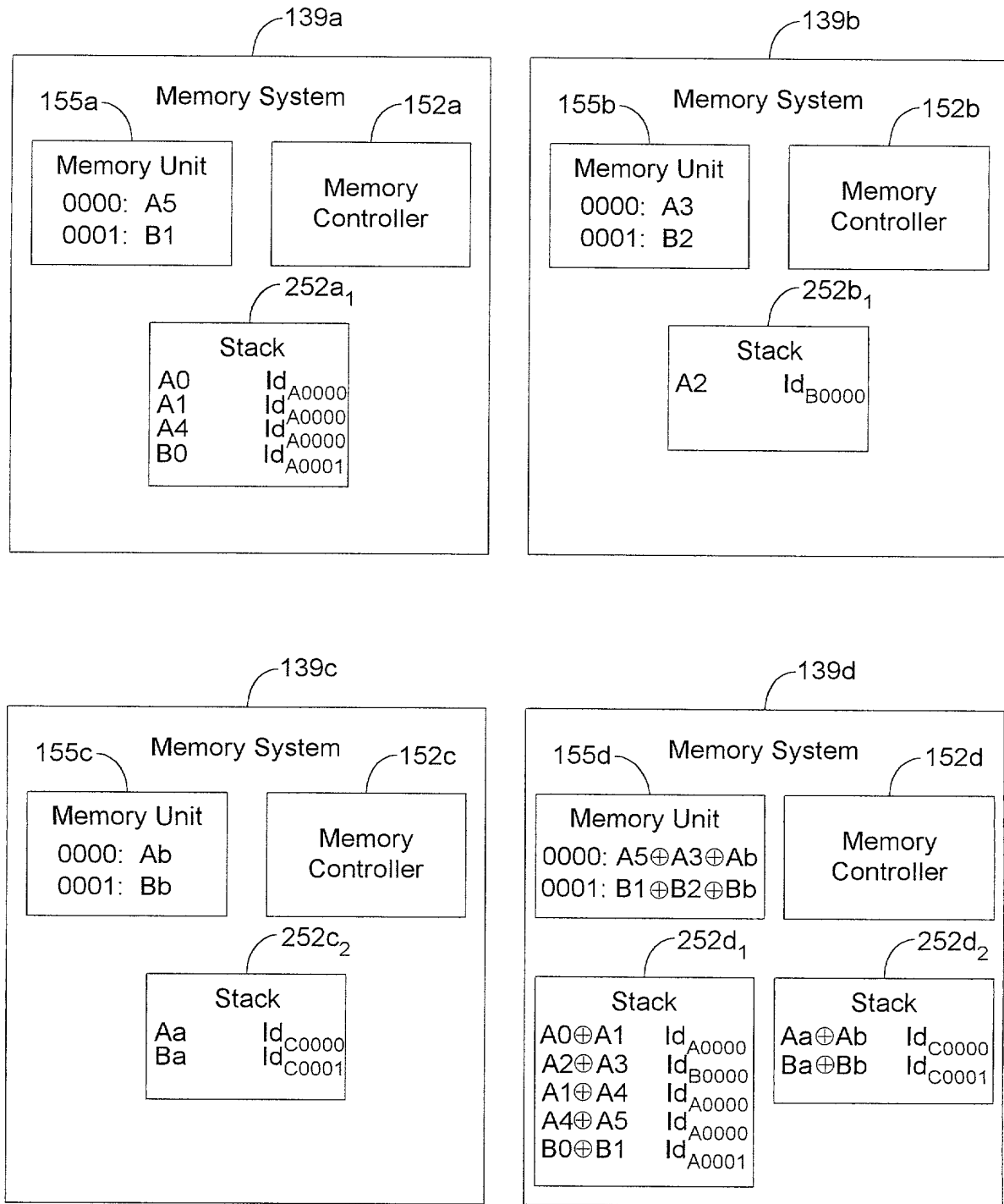


FIG. 16

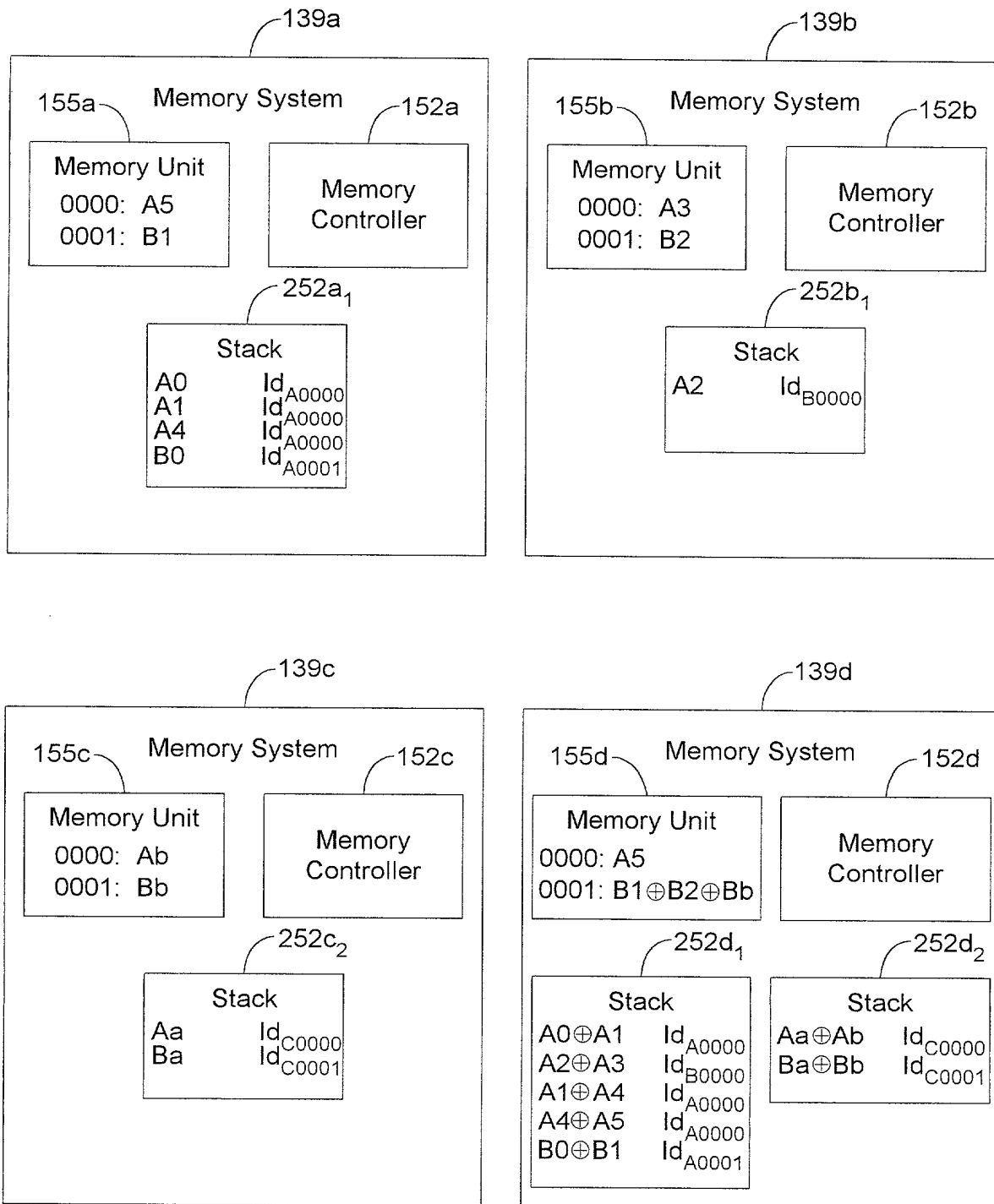
System And Method For Performing
Backward Error Recovery In A Computer

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**FIG. 17**

System And Method For Performing
Backward Error Recovery In A Computer

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**FIG. 18**

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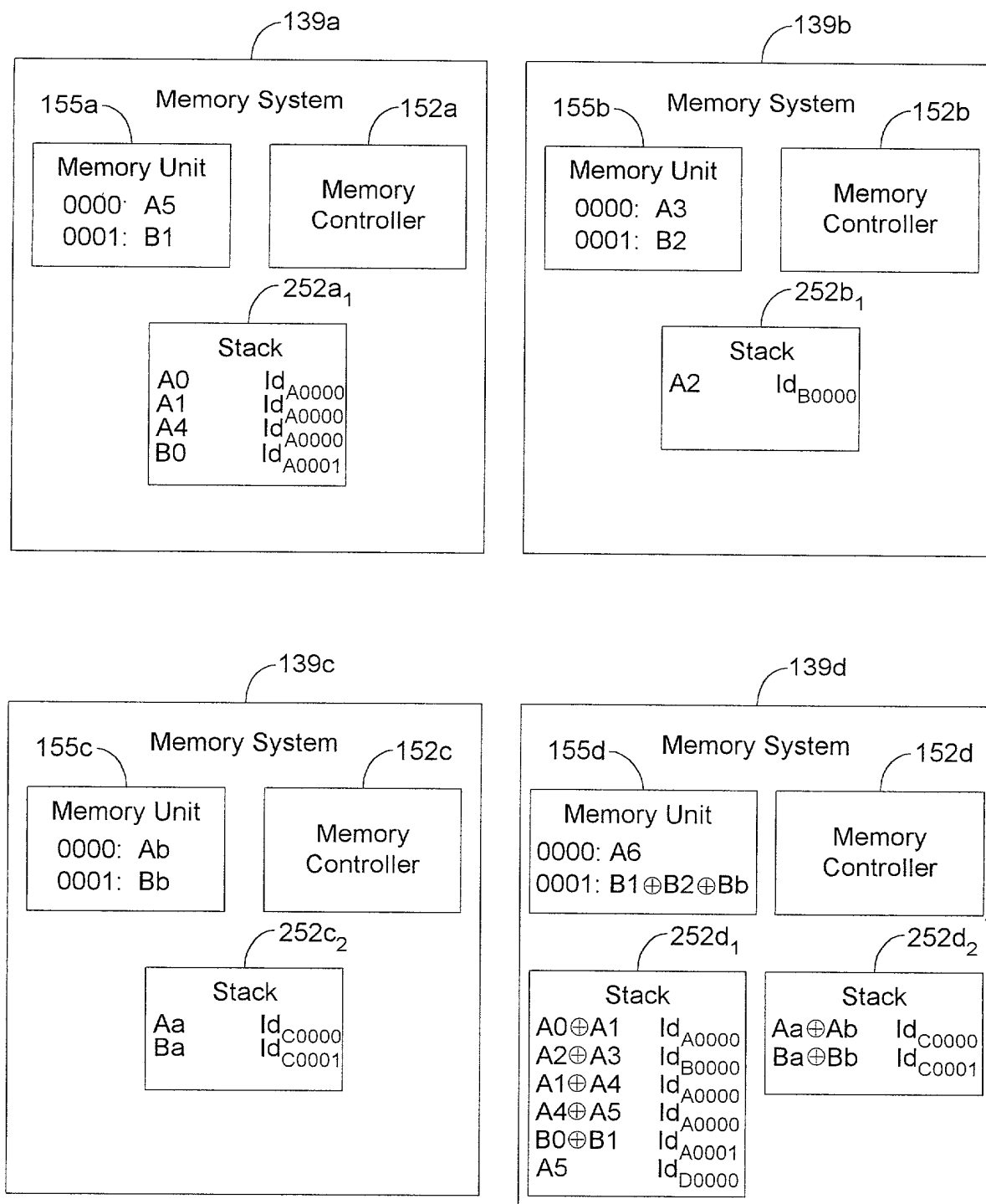
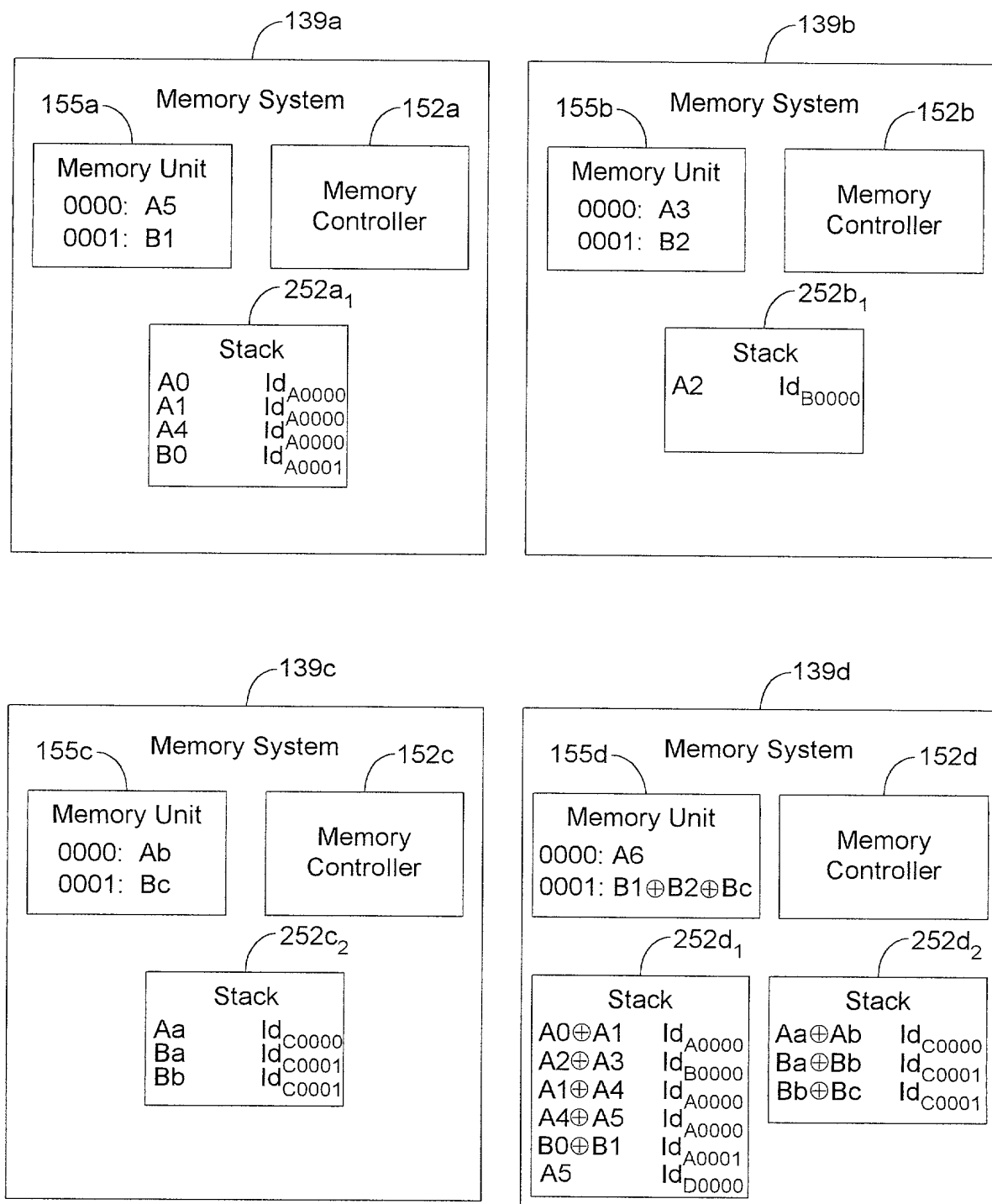


FIG. 19

**FIG. 20**

System And Method For Performing
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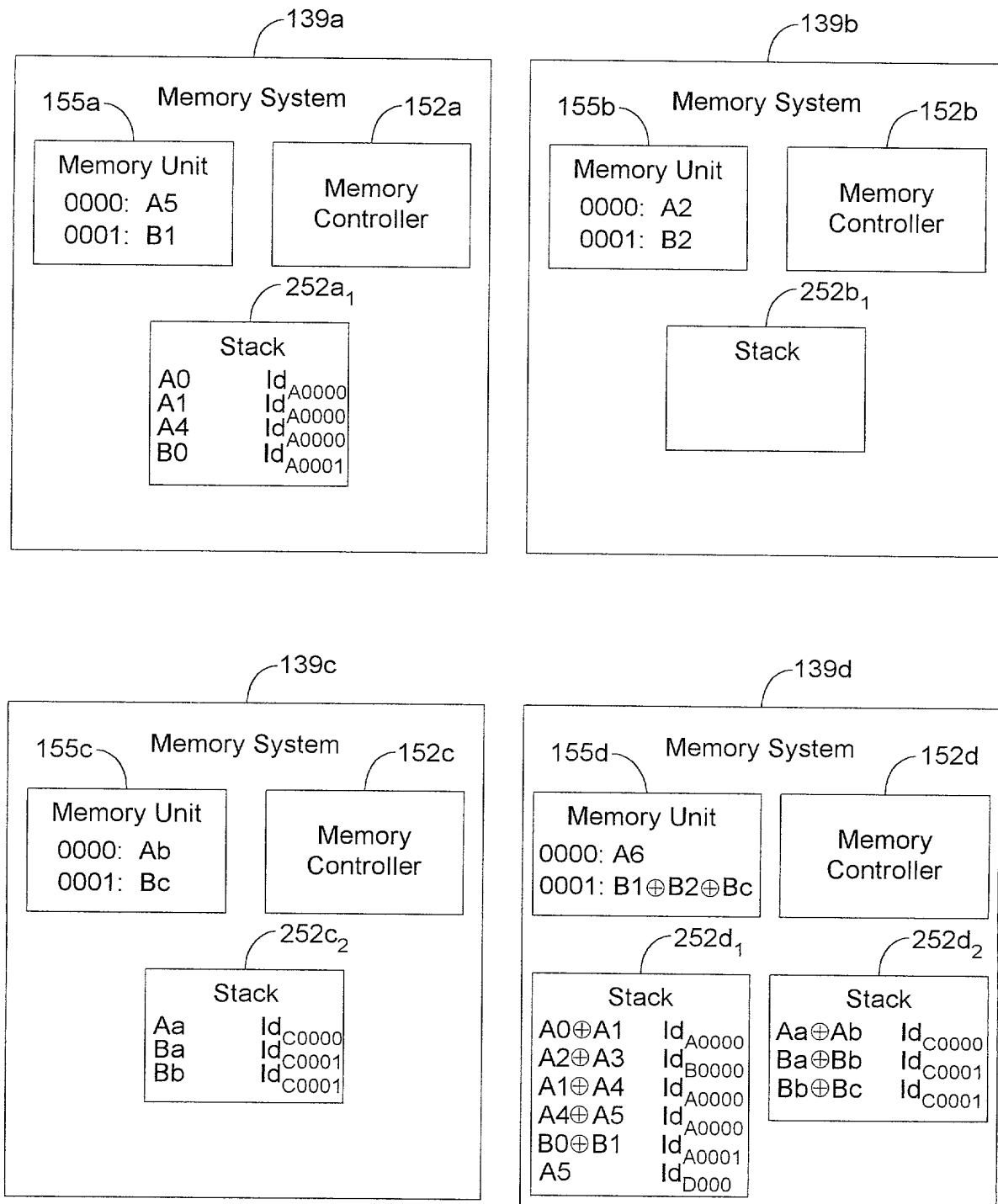
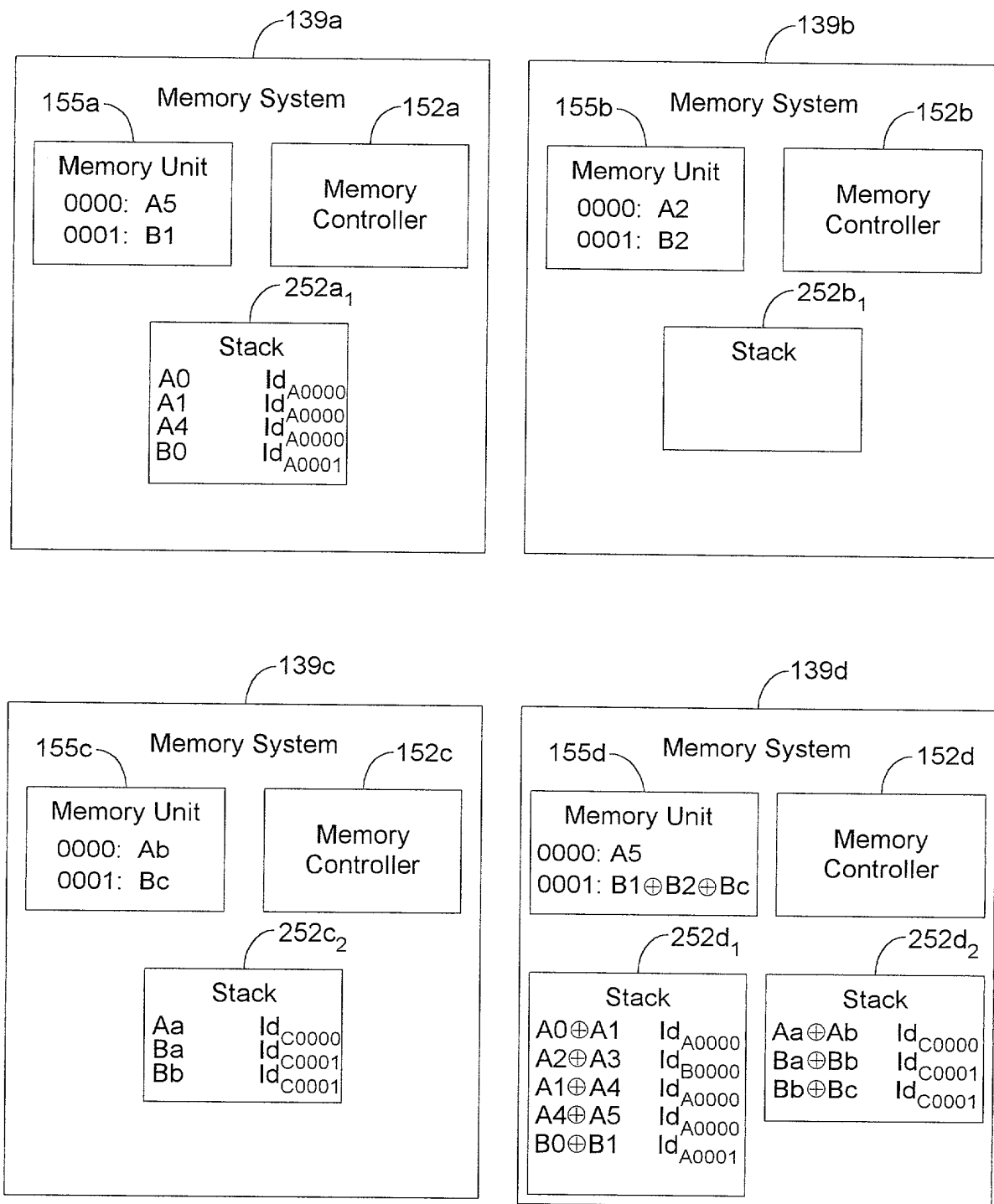
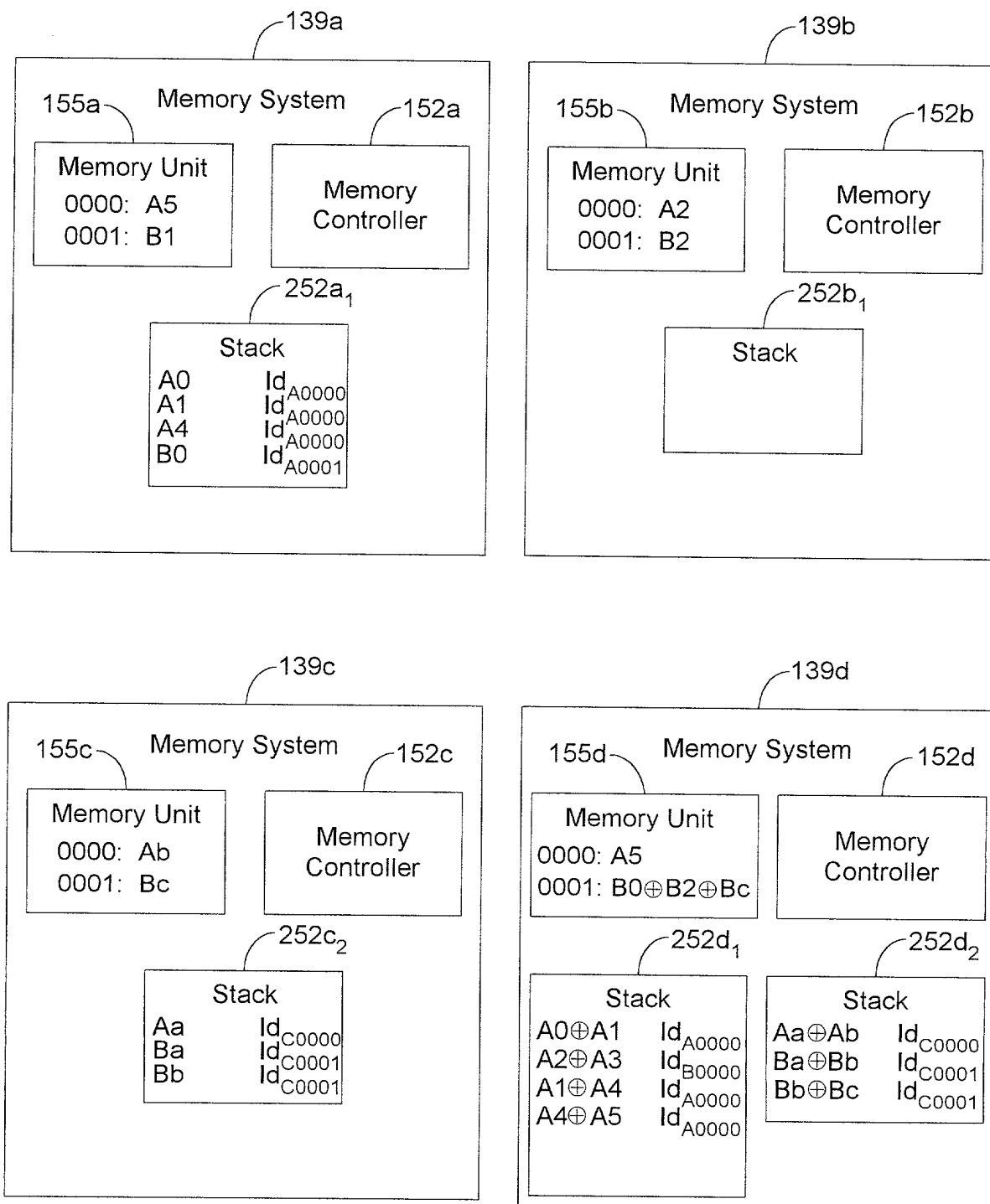
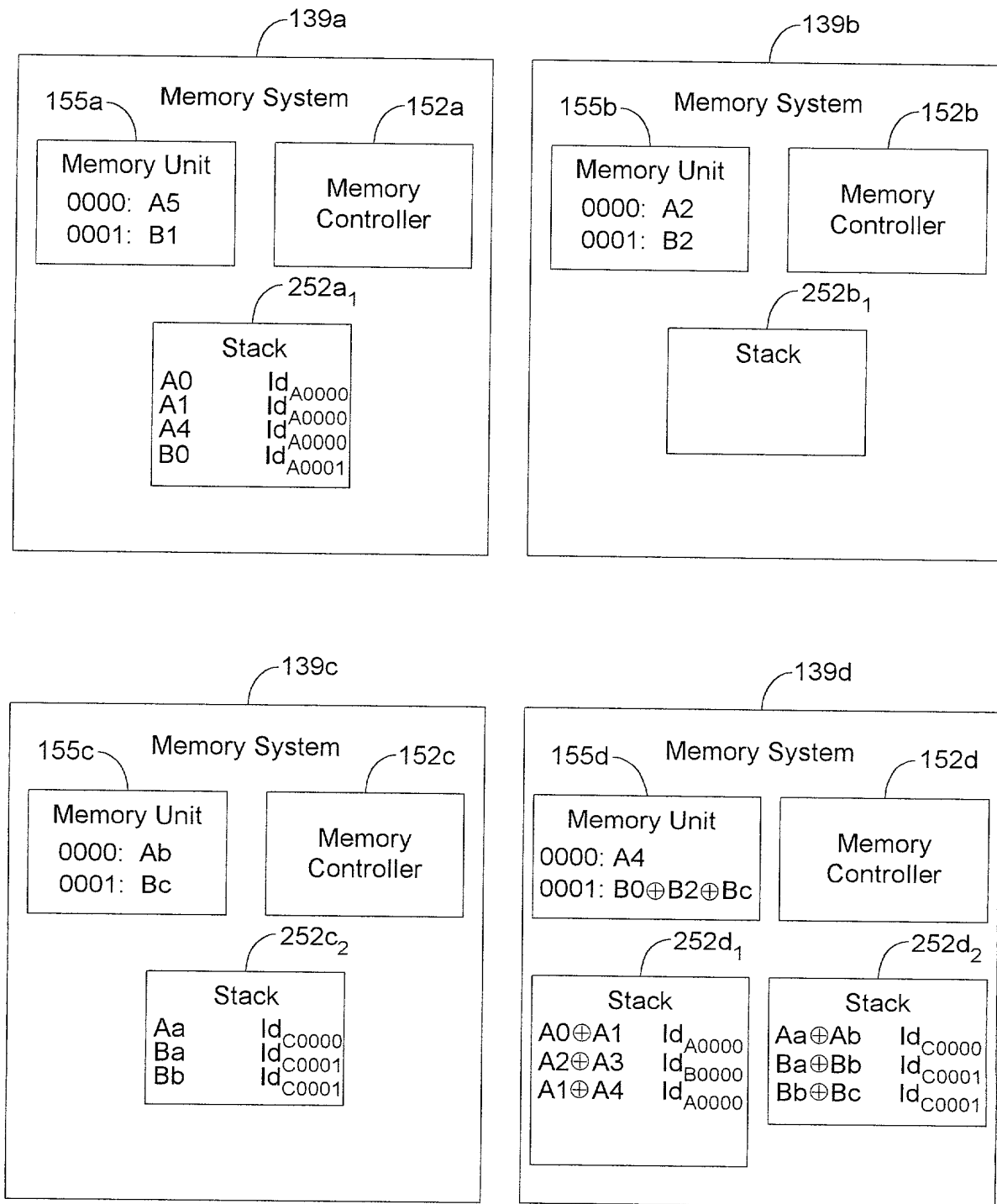
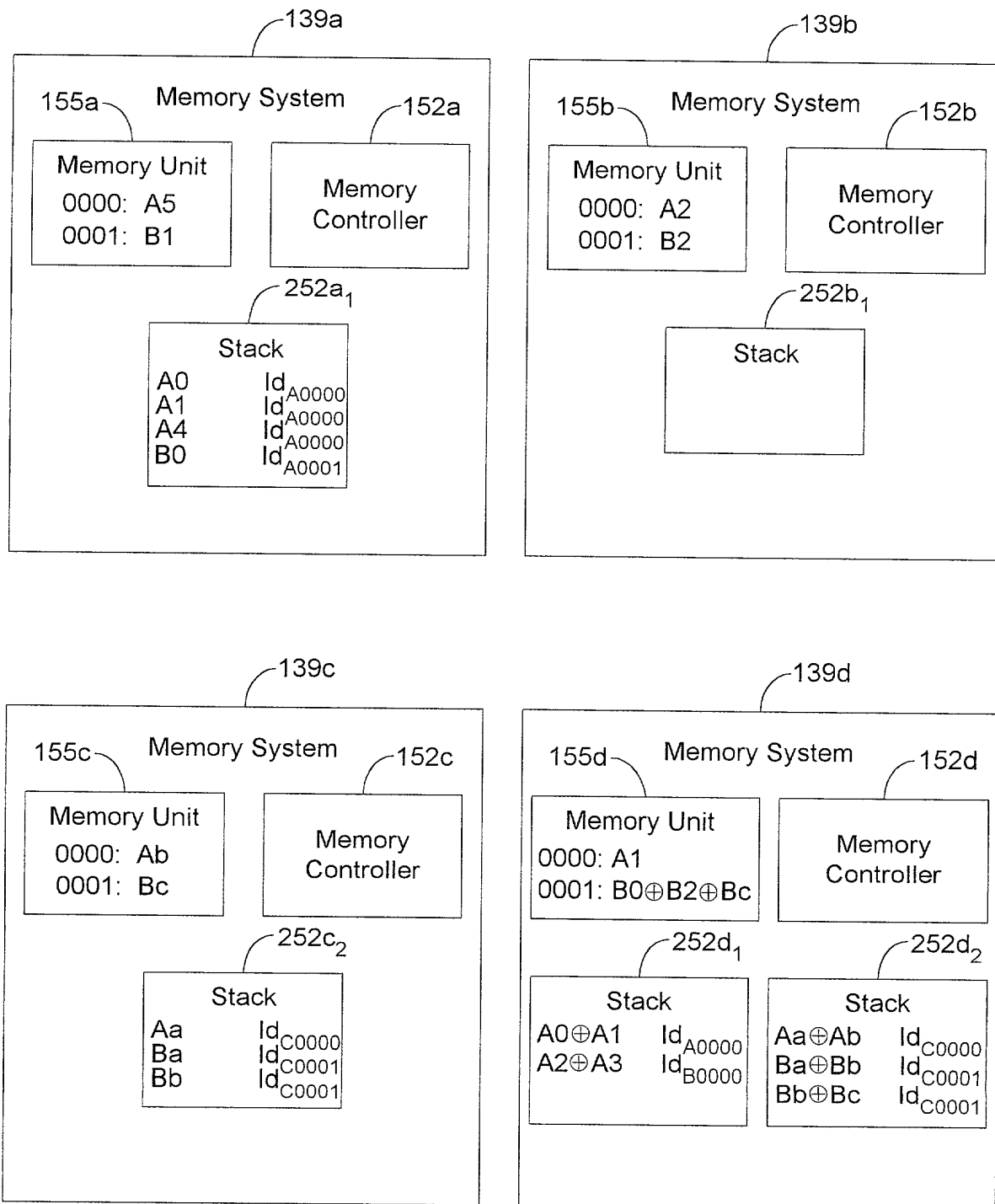


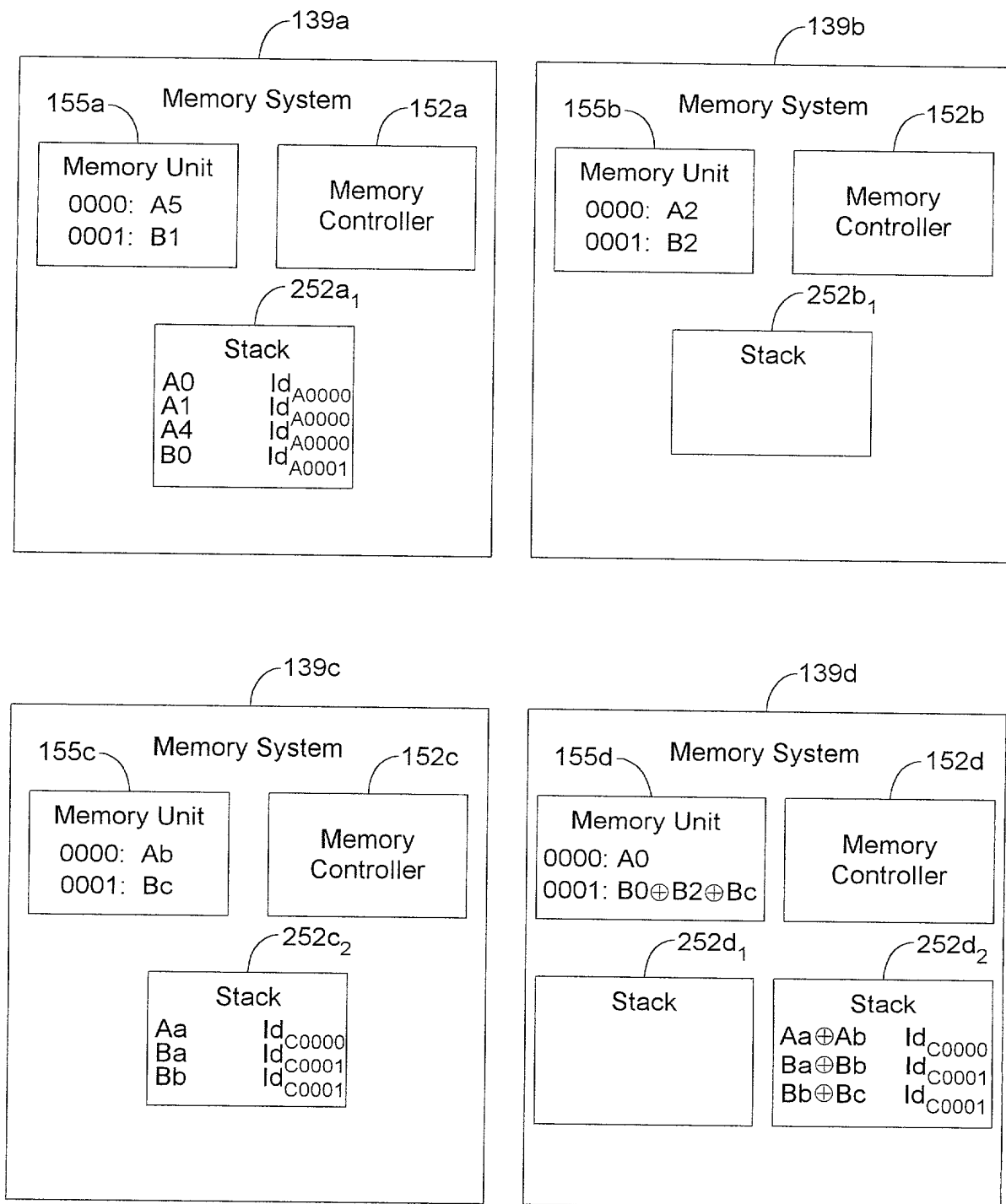
FIG. 21

**FIG. 22**

**FIG. 23**

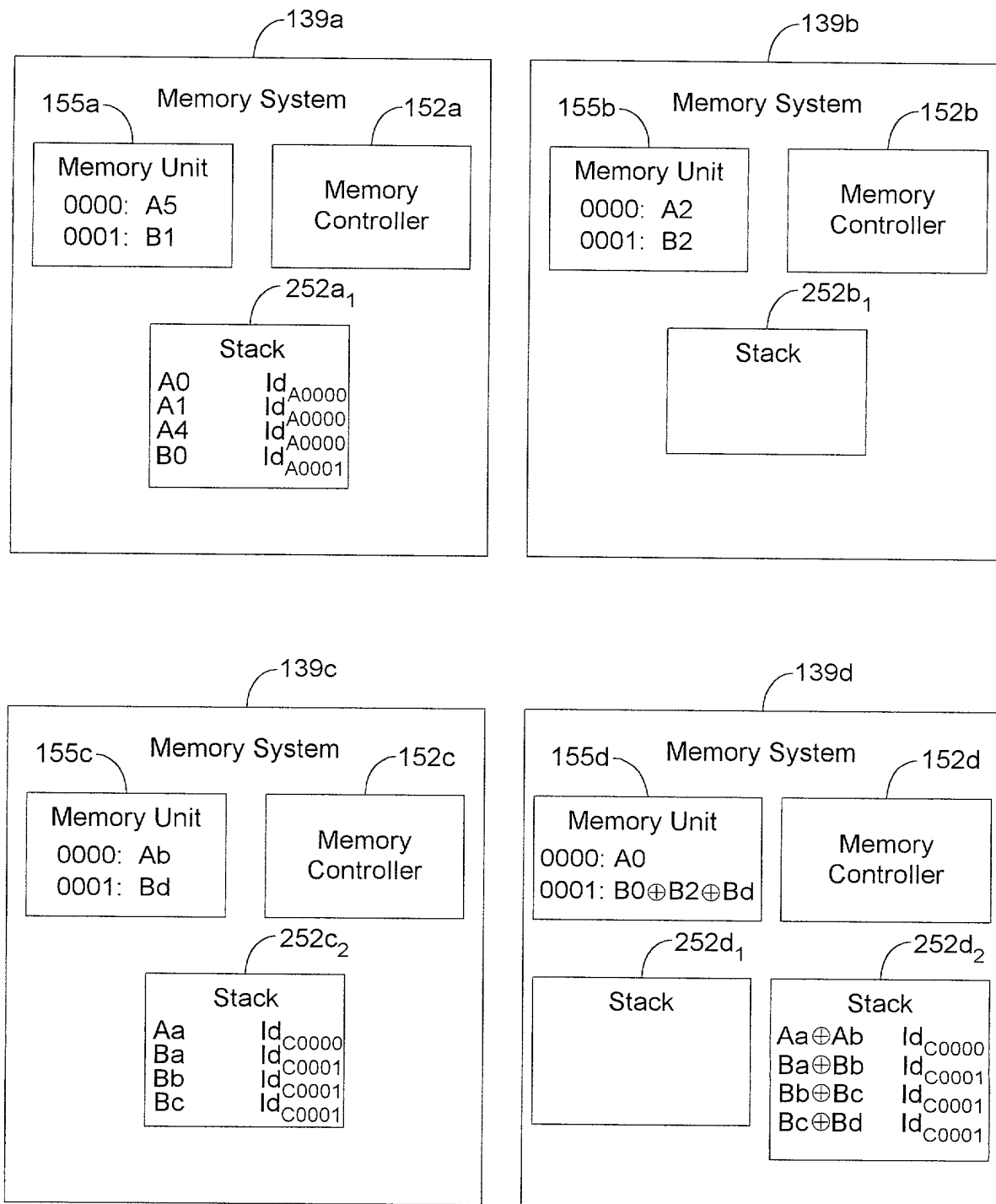
**FIG. 24**

**FIG. 25**

**FIG. 26**

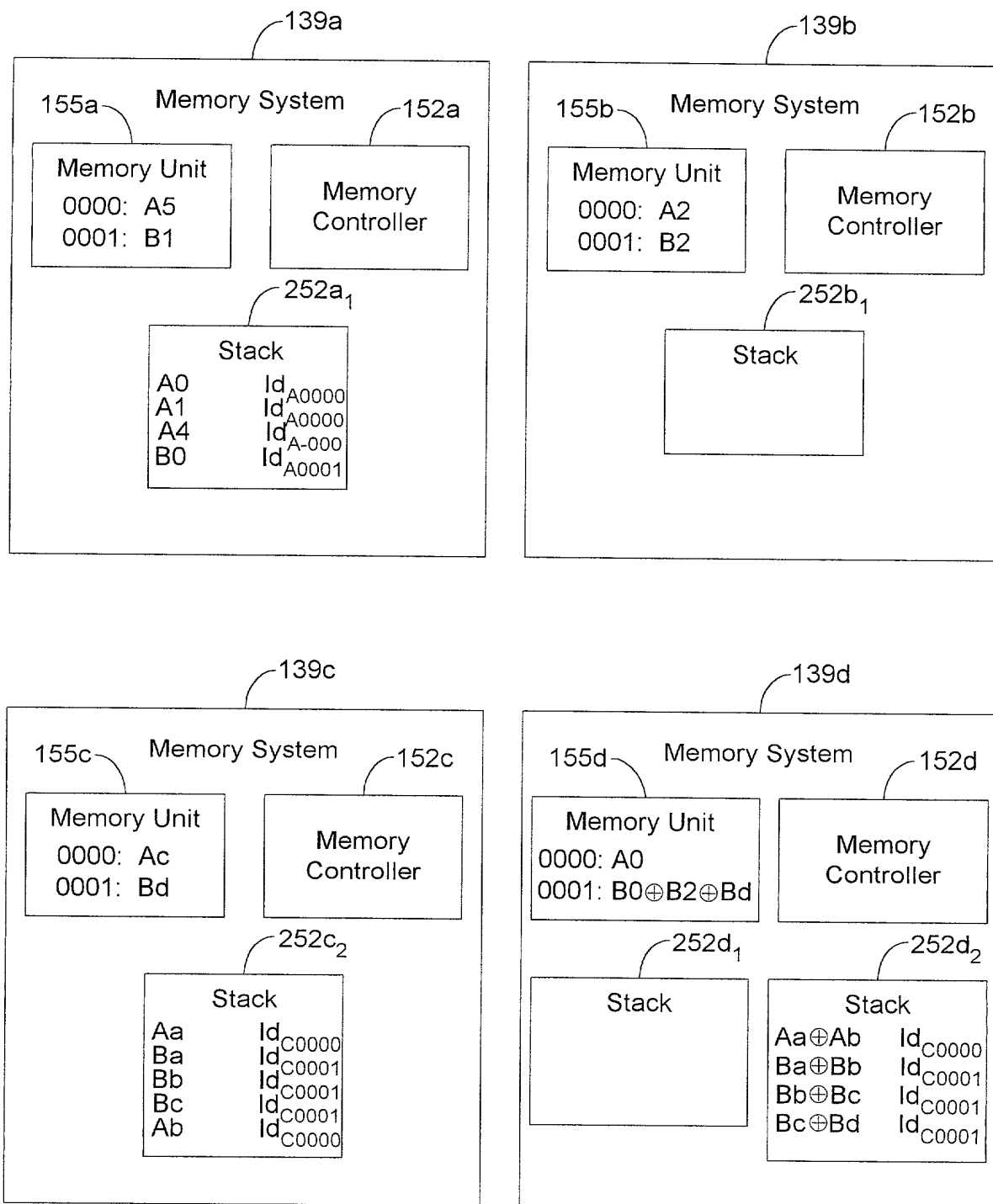
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**FIG. 27**

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**FIG. 28**